


Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 4640.P006	Application Number: 10/083,241
Page 1 of 2		First Named Inventor: Yervant Zorian	RECEIVED JUN 24 2002 Technology Center 2100
		Filing Date: February 25, 2002	

U.S. PATENT DOCUMENTS						
Exam. Initial ^a	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Patent of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (If known)			
DT		5,513,318		van de Goor et al.	4-30-1996	
DT		5,570,374		Yau et al.	10-29-1996	
DT		5,960,009		Gizopoulos et al.	9-28-1999	
DT		5,978,935		Kim et al.	11-2-1999	
DT		5,978,947		Kim et al.	11-2-1999	
DT		6,085,334		Giles et al.	7-4-2000	
DT		6,205,564	B1	Kim et al.	3-20-2001	
DT		6,237,123	B1	Kim et al.	5-22-2001	
DT		6,317,846	B1	Higgins et al.	11-13-2001	
DT		6,330,696	B1	Zorian et al.	12-11-2001	
DT		6,397,349	B2	Higgins et al.	5-28-2002	

FOREIGN PATENT DOCUMENTS								
Exam. Initial*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD- YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ⁵ (If known)				

Examiner Signature	<i>Davidson</i>	Date Considered	12/6/04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard S.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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Examiner Signature	<i>Dawson</i>	Date Considered	<i>12/6/04</i>
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Substitut f r Form 1449/PTO <div style="text-align: center; font-weight: bold; font-size: 1.2em;"> INFORMATION DISCLOSURE STATEMENT BY APPLICANT <small>(use as many sheets as necessary)</small> </div>				Complete if Known	
				Application Number	10/083,241
				Filing Date	February 25, 2002
				First Named Inventor:	Yervant Zorian
				Art Unit	2133
				Examiner Name	Unassigned
Sh et	of	2	Attorney Docket Number		04640.P006

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Number-Kind Code ² (if known)				
DT		US- 6,237,123 B1	05/22/2001	Kim, et al.		
DT		US- 6,205,564 B1	03/20/2001	Kim, et al.		
		US- 6,085,334	07/04/2000	Giles, et al.	<i>previously filed</i>	
DT		US- 6,317,846 B1	11/13/2001	Higgins, et al.		
DT		US- 5,570,374	10/29/1996	Yau, et al.	<div style="border: 1px solid black; padding: 5px; display: inline-block;"> RECEIVED JUL 17 2003 Technology Center 2100 </div>	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ³
		Country Code ⁴ Number ⁵ Kind Code ⁶ <small>(if known)</small>				

Examiner Signature <div style="text-align: center; font-family: cursive; font-size: 1.2em;">  </div>	Date Considered <div style="text-align: center; font-family: cursive; font-size: 1.2em;">  </div>	
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SENT FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner f r Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i> <div style="border: 1px solid black; border-radius: 50%; padding: 5px; width: 100px; text-align: center; float: left; margin-right: 10px;"> JUL 15 2003 U.S. PATENT & TRADEMARK OFFICE </div> <div style="clear: both;"></div>		Complete If Known	
		Application Number	10/083,241
		Filing Date	February 25, 2002
		First Named Inventor:	Yervant Zorian
		Art Unit	2133
		Examiner Name	Unassigned
		Attorney Docket Number	04640.P006
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
DT		Yervant Zorian: "System-Chip Test Strategies" (Tutorial): DAC 1998: 752-757.	
DT		Ilyoung Kim, Yervant Zorian, Goh Komoriya, Hai Pham, Frank P. Higgins, Jim L. Lewandowski: "Built-in Self-Repair for Embedded High Density SRAM." ITC 1998: 1112-1119.	
DT		Yervant Zorian, Erik Jan Marinissen, Sujit Dey: "Testing Embedded-Core Based System Chips." ITC 1998: pages 1-14.	
DT		Yervant Zorian, Erik Jan Marinissen, "System Chip Test: How Will It Impact Your Design?" pages 1-6, DAC, 2000.	
DT		Monica Lobetti Bodoni, Alfredo Benso, Silvia Chiusano, Stefano DiCarlo, Giorgio DiNatale, Paolo Prinetto, "An Effective Distributed BIST Architecture for RAMs," pages 1-6, IEEE European Test Workshop, 2000.	
DT		Chauhin Su, Shih-Ching Hsiao, Hau-Zen Zhau, Chung-Len Lee, "A Computer Aided Engineering System for Memory BIST," pages 1-4, DAC, 2001.	

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